

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>
1	L2	64	1 and register
2	L1	237	single adj event adj upset
3	L3	466	single adj event adj upset
4	L4	112	3 and register
5	L5	29	NAND adj gate adj SR
6	L6	7	NOR adj gate adj SR
7	L7	686	SR adj flip adj flop
8	L8	250	7 and NAND
9	L9	1	8 and 3
10	L10	23	8 and redundant
11	L11	2	7 and 3
12	L12	1037	((RS adj flip adj flop) or (SR adj flip adj flop)) and gate and feedback
13	L13	75	12 and redundant
14	L14	354	seu
15	L15	4744	seu
16	L16	1324	((RS adj flip adj flop) or (SR adj flip adj flop)) and gate and feedback
17	L17	2	15 and 16